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Application Number:	10072145			
International Application Number:				
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Title of Invention:	Semiconductor device and method of providing regions of low substrate capacitance			
First Named Inventor/Applicant Name:	Guy E. Averett			
Correspondence Address:	ON Semiconductor Patent Administration Dept - MD A700 P.O. Box 62890 Phoenix AZ 85082-2890 US 6022445603 -			
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New International Application Filed with the USPTO as a Receiving Office

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